

In Claim 12, line 2, delete "dual-aperture" and insert therefor -- first --.

In Claim 23, line 3, delete "playback video"; and

In Claim 23, line 10, delete "dual-aperture".

Please add new Claims 33-47 as follows.

Sub 3 } 33. (New) The system of Claim 1 wherein said port comprises a dual-aperture port.

24 } 34. (New) The controller of Claim 12 wherein said first port comprises a dual-aperture port.

32 } 35. (New) The display system of Claim 25 wherein said first backend pipeline processes playback video.

AI cont } 33 } 36. (New) The display system of Claim 25 wherein said input port comprises a dual-aperture input port.

37. (New) A display controller comprising:
circuitry for selectively retrieving data from an associated multi-format frame buffer for simultaneously storing graphics and video data;
a first pipeline for processing words of graphics data selectively retrieved from said frame buffer; and
a second pipeline for processing words of video data selectively retrieved from said frame buffer.

38. (New) The controller of Claim 37 wherein said first and second pipelines are disposed in parallel and concurrently process data.

✓ 39. (New) The controller of Claim 38 and further comprising output selection circuitry for selecting for output between graphics data received from said first pipeline and video data received from said second pipeline.

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cont 40. (New) The controller of Claim 37 wherein said frame buffer is partitioned into on-screen and off-screen areas, each of said on-screen and off-screen areas operable to simultaneously store both graphics and video data.

41. (New) The controller of Claim 37 wherein said circuitry for selectively retrieving is operable to retrieve a constant stream of graphics data from said frame buffer and provide said stream of graphics data to said first pipeline.

42. (New) The controller of Claim 41 wherein said circuitry for selectively retrieving is operable to retrieve at least one said word of video data from said frame buffer and provide said at least one word of said video data to said second pipeline, only when said display controller is generating a video display window.

43. (New) A display controller for interfacing a multi-format frame buffer and a display device, the multi-format frame buffer having on-screen and off-screen areas each for simultaneously storing both graphics and video pixel data, said display controller comprising:

circuitry for selectively retrieving pixel data from a selected one of said on-screen and off-screen areas of said frame buffer;

a graphics backend pipeline for processing graphics data retrieved from said selected one of said areas of said frame buffer;

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cont a video backend pipeline for processing video data retrieved from said selected one of said areas of said frame buffer; and

an output selector for selectively passing to said display device data received from said graphics or video backend pipelines.

44. (New) The display controller of Claim 43 wherein said circuitry for selectively retrieving is operable to retrieve at least one said word of video data from said frame buffer and provide said at least one said word of video data to said second pipeline only when said display controller is generating a video display window.

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45. (New) The display controller of Claim 43 wherein said output selector is operable to:

in a first mode, pass data from said graphics pipeline; and

in a second mode, pass data from said video pipeline when a display position corresponding to a video display window has been reached.

46. (New) The display window of Claim 43 wherein said output selector is operable to:

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in a first mode, pass data from said graphics pipeline; and

in a second mode, pass data from said video pipeline when a display position corresponding to a video display window has been reached and data from said graphics pipeline match a color key.

47. (New) The display controller of Claim 43 wherein said output selector is operable to:

in a first mode, pass data from said graphics pipeline; and

in a second mode, pass data from said video pipeline when data from said graphics pipeline matches a color key.